	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
1	BRS	L1	73	(first adj3 DLL) same (second adj3 DLL)	USPAT; US-PGPUB	2004/03/18 13:11	
2	BRS	L2	566	(first adj3 PLL) same (second adj3 PLL)	USPAT; US-PGPUB	2004/03/18 13:12	
3	BRS	L3	116	first adj2 delay$2 adj lock$2 adj loop$2	USPAT; US-PGPUB	2004/03/18 13:13	
4	BRS	L4	79	second adj2 delay$2 adj lock$2 adj loop$2	USPAT; US-PGPUB	2004/03/18 13:14	
5	BRS	L5	1272	second adj2 phase$2 adj lock$2 adj loop$2	USPAT; US-PGPUB	2004/03/18 13:15	
6	BRS	L6	1096	first adj2 phase$2 adj lock$2 adj loop$2	USPAT; US-PGPUB	2004/03/18 13:16	
7	BRS	L7	507	5 same 6	USPAT; US-PGPUB	2004/03/18 13:16	
8	BRS	L8	56	3 same 4	USPAT; US-PGPUB	2004/03/18 13:16	
9	BRS	L9	7	1 and 8	USPAT; US-PGPUB	2004/03/18 13:40	
10	BRS	L10	122	1 or 8	USPAT; US-PGPUB	2004/03/18 13:40	
11	BRS	L11	1000	2 or 7	USPAT; US-PGPUB	2004/03/18 13:41	
12	BRS	L12	48438	(control$6 or programmable) near3 delay$3	USPAT; US-PGPUB	2004/03/18 13:42	
13	BRS	L13	143	11 and 12	USPAT; US-PGPUB	2004/03/18 13:42	

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
14	BRS	L14	265	10 or 13	USPAT; US-PGPUB	2004/03/18 13:43	
15	BRS	L15	8840	("90" adj degree$1) near5 phase	USPAT; US-PGPUB	2004/03/18 13:44	
16	BRS	L16	300	(quarter adj3 cycle) near5 clock	USPAT; US-PGPUB	2004/03/18 13:45	
17	BRS	L17	19	14 and (15 or 16)	USPAT; US-PGPUB	2004/03/18 14:31	
18	BRS	L18	82514	(captur$3 or sampl$3) near3 data	USPAT; US-PGPUB	2004/03/18 14:32	
19	BRS	L19	19445	(captur$3 or sampl$3) near3 clock	USPAT; US-PGPUB	2004/03/18 14:32	
20	BRS	L20	9715	18 and 19	USPAT; US-PGPUB	2004/03/18 14:33	
21	BRS	L21	40	14 and 20	USPAT; US-PGPUB	2004/03/18 14:34	
22	BRS	L22	1438	clock near5 tree	USPAT; US-PGPUB	2004/03/18 14:34	
23	BRS	L23	1	21 and 22	USPAT; US-PGPUB	2004/03/18 14:35	
24	BRS	L24	39	21 not 23	USPAT; US-PGPUB	2004/03/18 15:06	
25	BRS	L25	4493	model near10 delay$3	USPAT; US-PGPUB	2004/03/18 15:10	
26	BRS	L26	21	14 and 25	USPAT; US-PGPUB	2004/03/18 15:16	

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
27	BRS	L27	10	14 and 22	USPAT; US-PGPUB	2004/03/18 15:39	
28	BRS	L28	25941	memory same (memory adj controller)	USPAT; US-PGPUB	2004/03/18 15:40	
29	BRS	L29	40	14 and 28	USPAT; US-PGPUB	2004/03/18 15:40	
30	BRS	L30	19	29 not (21 or 26 or 27)	USPAT; US-PGPUB	2004/03/18 15:58	
31	BRS	L31	2016	713/400-501.ccls.	USPAT; US-PGPUB	2004/03/18 15:59	
32	BRS	L32	898	713/503-600.ccls.	USPAT; US-PGPUB	2004/03/18 15:59	
33	BRS	L33	2544	31 or 32	USPAT; US-PGPUB	2004/03/18 15:59	
34	BRS	L34	14	14 and 33	USPAT; US-PGPUB	2004/03/18 15:59	